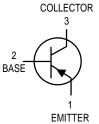
# **High Voltage Transistors** PNP Silicon



#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	VCEO	-300	Vdc	
Collector-Base Voltage	Vсво	-300	Vdc	
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	Vdc	
Collector Current — Continuous	IC	-500	mAdc	
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C	
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	1.5 12	Watts mW/°C	
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>Stg</sub>	-55 to +150	°C	

# **PBF493 PBF493S**



## THERMAL CHARACTERISTICS

Characteristic	Symbol Max		Unit	
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	200	°C/W	
Thermal Resistance, Junction to Case	$R_{ heta}$ JC	83.3	°C/W	

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage (1) (IC = -1.0 mAdc, I <sub>B</sub> = 0)	V(BR)CEO	-300	_	Vdc	
Collector-Base Breakdown Voltage (I <sub>C</sub> = -10 μAdc, I <sub>E</sub> = 0)	V(BR)CBO	-300	_	Vdc	
Emitter-Base Breakdown Voltage (IE = $-100 \mu Adc$ , IC = 0)	V(BR)EBO	-5.0	_	Vdc	
Collector Cutoff Current (V <sub>CB</sub> = -200 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	_	-0.25	μAdc	
Emitter Cutoff Current (VEB = -3.0 Vdc)	I <sub>EBO</sub>	_	-20	nAdc	
Collector Cutoff Current (V <sub>CE</sub> = -10 Vdc)	ICEO	_	-250	nAdc	

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s; Duty Cycle  $\leq$  2.0%.



# **PBF493 PBF493S**

# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS					
DC Current Gain $ \begin{aligned} &(I_C = -0.1 \text{ mAdc}, V_{CE} = -1.0 \text{ Vdc}) \\ &(I_C = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}) \\ &(I_C = -30 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}) \end{aligned} $	PBF493S All Types All Types	hFE	40 40 25	_ _ _	_
Collector-Emitter Saturation Voltage (IC = -20 mAdc, I <sub>B</sub> = -2.0 mAdc)		VCE(sat)	_	-0.5	Vdc
Base-Emitter Saturation Voltage (IC = -20 mAdc, I <sub>B</sub> = -2.0 mAdc)		V <sub>BE</sub> (sat)	_	-0.9	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain — Bandwidth Product (IC = -10 mAdc, VCE = -20 Vdc, f = 20 MHz)		fT	50	_	MHz
Output Capacitance (V <sub>CB</sub> = -20 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)		C <sub>obo</sub>	_	6.0	pF

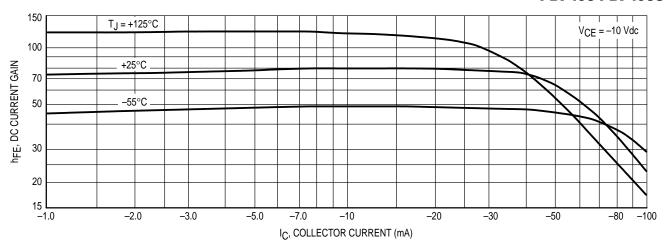


Figure 1. DC Current Gain

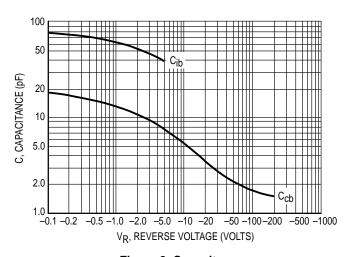


Figure 2. Capacitances

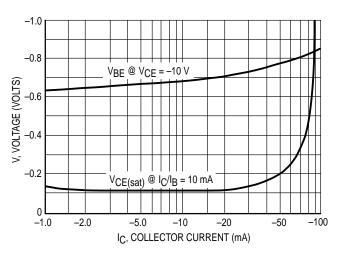


Figure 4. "On" Voltages

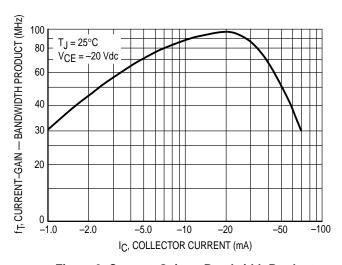


Figure 3. Current-Gain — Bandwidth Product

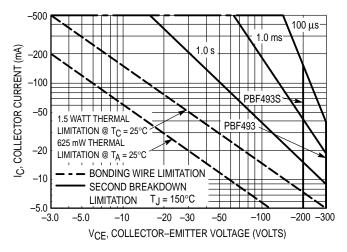
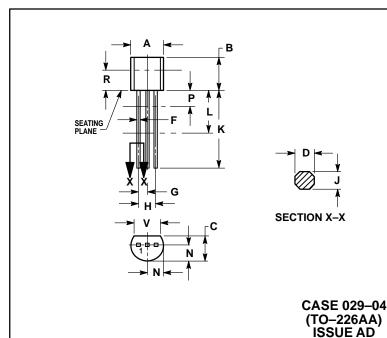


Figure 5. Active Region — Safe Operating Area

#### PACKAGE DIMENSIONS



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
  CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN P AND L. DIMENSION P APPLIES BETWEEN F AND L.
  DIMENSION D AND J APPLY BETWEEN L AND K
  MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0.135		3 43	

STYLE 1:

BASE

PIN 1. EMITTER 3. COLLECTOR

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### How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1-800-441-2447 or 602-303-5454

MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE 602-244-6609 INTERNET: http://Design-NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi-SPD-JLDC, 6F Seibu-Butsuryu-Center, 3-14-2 Tatsumi Koto-Ku, Tokyo 135, Japan. 03-81-3521-8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298



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